**EE524/CptS561 Fall 2012**

**Project Report Due: December 14, 2012 (4PM)**

Please include the following items in your report:

1. Introduction

Brief description of the scheme (i.e. Tomasulo, reorder buffer and speculation).

Brief description of your project (major components of your simulator)

1. Parameters that are observed:
   * Number of instructions issued per cycle (average)
   * Number of instructions in write back per cycle (average)
   * Number of instructions that are committed per cycle (average)
   * Every four cycles print the status of the reorder buffer. There is field in the ROB call instruction; here you could either include the instruction or include the address of the instruction. For instance in your first ROB entry (form your benchmark) you will have either: *L.D F2, 0(R1)* or *00*.
2. **Discussion of results**

Please describe in detail your findings (use a quantitative approach to discuss the impact of your findings)

-impact on CPI ? (ideal and obtained CPI).

-in your simulation what limited to have higher ILP. (could be: issue, reservation stations, write back, commit,…)

1. References/bibliography

You may find some good references at: http://ieeexplore.ieee.org