I. Assessment Outcomes from the Course Syllabus

- (A) Ability to apply knowledge of mathematics, science and engineering.
- (B) Ability to design and conduct experiments as well as analyze and interpret data.
- (C) Ability to design a system, component, or process to meet desired needs.
- (D) Ability to function on multidisciplinary teams.
- (E) Ability to identify, formulate, and solve engineering problems.
- (F) An understanding of professional and ethical responsibility.
- (G) Ability to communicate effectively in written and oral formats.
- (H) A broad education necessary to understand the impact of engineering solutions in global, economic, and societal context.
- (I) Recognize the need for, and have the ability to engage in life long learning.
- (J) Have a broad education and knowledge of contemporary issues.
- (K) Ability to use techniques, skills and modern engineering tools necessary for engineering practices.

II. List of Course Topics from the Course Syllabus

1. Introduction to electronics.
2. Operational amplifiers.
3. Diodes: physics, small-signal model, applications.
4. BJTs: physics, biasing, small-signal model.
5. BJT single-stage amplifiers: analysis and design.
6. BJT current mirrors.
7. BJT differential amplifiers: analysis and design.
8. MOSFETs: physics, biasing, small-signal model.
9. MOSFET single-stage amplifiers: analysis and design.
10. MOSFET current mirrors and differential amplifiers.
11. Frequency response.
III. Course Assessment Summary Table: one row of the table should be devoted to each of the checked outcomes in part 1.

<table>
<thead>
<tr>
<th>Outcome</th>
<th>Topics</th>
<th>Specific Measures (Samples should be available in the course materials file for inspection.)</th>
</tr>
</thead>
<tbody>
<tr>
<td>(A) Ability to apply knowledge of mathematics, science and engineering.</td>
<td>2-11</td>
<td>Most homework, quizzes and exams</td>
</tr>
<tr>
<td>(C) Ability to design a system, component, or process to meet desired needs.</td>
<td>2-11</td>
<td>HW 6, 8 &amp; 18 Q 3 &amp; 6, E 2.2, 2.3, Final. 2</td>
</tr>
<tr>
<td>(H), (I), and (J)</td>
<td></td>
<td>None</td>
</tr>
<tr>
<td>(K) Ability to use techniques, skills and modern engineering tools necessary for engineering practices.</td>
<td>2 - 11</td>
<td>Q2, 4, 5, 7, 8 E1.1, 1.2, 2.1, 2.5 F1, 4, 5, 8, 9</td>
</tr>
</tbody>
</table>

IV. Using the table as a guide, for each outcome summarize your evaluation of the students’ achievement of that outcome; cite student performance on the identified measures as evidence to support your conclusions.

(A) Ability to apply knowledge of mathematics, science and engineering.

The entire course involves the application of mathematics to analyze and design amplifier circuits and determine their performance parameters. The underlying behavior for diodes and transistors is the physics of the device. This ability can be assessed by the students’ overall average.

(C) Ability to design a system, component, or process to meet desired needs.

Although most of the course emphasizes analysis of circuits, more emphasis is directed to design in the latter part of the course. 40% of exam 2 and quizzes 3 and 6 include design related questions.

(H) A broad education necessary to understand the impact of engineering solutions in global, economic, and societal context.

(I) Recognize the need for, and have the ability to engage in life long learning.

(J) Have a broad education and knowledge of contemporary issues.

There are no specific measures of these three outcomes in this course. It is recommended that these outcomes be removed from the syllabus for EE311.

(K) Ability to use techniques, skills and modern engineering tools necessary for engineering practices.
Techniques introduced in this class include small and large signal models, small signal analysis, zero value time constants, Miller’s theorem and summing point constraints for op-amps. Half of the final exam questions concern these techniques.

V. Qualitative Assessment of Student Performance: using the arguments above and other data support the claim that students who completed this course with a grade of C or better have achieved each of the intended outcomes of this course.

The final comprehensive examination is used as assessment tool of student’s performance and comparing them to other groups. The highest score was 116, the mean score was 80, the lowest score was 21, and the standard deviation was 23. Figure 1 shows the histogram of scores for the assessment exam. This class had the same mean and high score as last year, which is a 3% improvement over spring 2003 and spring 2004. The standard deviation is back up to near 20 this year and is the same as last year after being 12 two years ago. The percentage of students scoring 90 or above is 37% compared to 33%, 7% and 23% the previous 3 years. The percentage scoring below 60 was 17%, which is not significantly different than the 15%, 14% and 10% percentages the previous 3 years. Although the students scoring below 60 was slightly higher, only 3 students scored lower than 55 this year compared to 6 students last year.

![Figure 1. Histogram of assessment exam scores from EE311 Spring 2006.](image)

The lowest grade on the final of a passing student was 57 out of 120. The lowest overall weighted average including homework, quizzes, exams and a comprehensive final was 60%. Assessing Outcome A is based on the overall average, which is greater than 60% for every student. The final is a good assessment for Outcome K, since over half the questions concern techniques to analyze circuits. The minimum score was about 50%, which shows some competence in using these skills. Outcome C is not a major objective of this course but directly impacts about 15% of their overall average. Although it is possible for a student to have a passing overall average of 60% while scoring 0% on design related problems, the student would need an average of 70% in the non-design
related 85% of the score. However, it is my experience that the students that can analyze circuits better are also better able to reverse the process and design circuits.

VI. Concerns: state any concerns you may hold about this class – were the students adequately prepared coming into it? Are there topics or outcomes where (some) students were weak after completing the course? Other concerns? Were there any comments on students’ course evaluations that should be addressed in future instances of the course? This section is very important for improving our program: it provides critical input to the curriculum committee for identifying areas requiring attention.

I reduced the number of quizzes from 11 to 9 and the number of exams from 3 to 2 in order to allow more time for working examples in lecture. I think it would be better to go back to 3 exams and having 10 or 11 quizzes.

I provided weekly review and problem solving sessions in the evening starting about halfway through the class. The students appreciated these sessions.

I was pleased with the textbook change to Sedra and Smith, Fifth Edition.

I recommend that we continue giving homework assignments every class.

I recommend that we remove outcomes (H), (I) and (J) from the syllabus for EE311.

Signature __________________________________________ Date: ___________________________

Please email a copy of the completed form to Patricia Arnold, patricia@eeecs.wsu.edu and deliver a signed hardcopy to her mailbox.